

<b>Notice of References Cited</b>	Application/Control No. 10/699,585	Applicant(s)/Patent Under Reexamination HEZAR ET AL.	
	Examiner Howard L. Williams	Art Unit 2819	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,227,795	07-1993	Yamakido et al.	341/166
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	White et al., Integrated fourth-order Sigma-Delta convertor with stable self-tuning continuous time noise shaper, IEE Proceedings on Circuit, Devices & Systems, vol. 141, no. 3 June 1994, pages 145-150.
	V	Schreier et al., Delta-Sigma Modulators Employing Continuous-Time Circuitry, IEEE Transactions on Circuits and Systems-I: Fundamental Theory and Applications, Vol. 43, No. 4, April 1996, pages 324-332.
	W	Benabes et al. Passive Sigma-Delta converters design, IEEE Instrumentation and Measurement Technology Conference, 21-23 May 2002, pages 469-474.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.